

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Application of: CHIU et al. |) | Examiner: Not yet assigned |
| |) | |
| Serial No.: Not yet assigned |) | Group Art Unit: Not yet assigned |
| |) | |
| Filing Date: Herewith |) | Attorney Docket No.: P16585 |
| |) | |
| For: POWER SUPPLY SIGNAL FROM |) | |
| SYSTEM SIDE CIRCUIT TO LINE SIDE |) | |
| CIRCUIT IN TELECOMMUNICATION |) | |
| DEVICE |) | |
| |) | |
| |) | |

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants submit herewith patents, publications or other information of which they are aware that they believe may be material to the examination of this application, and in respect of which, there may be a duty to disclose.


The filing of this information disclosure statement shall not be construed as a representation that a thorough search has been made, an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists. Nor shall the filing of this information disclosure statement be construed as an admission against interest in any manner.

This Information Disclosure Statement is filed in accordance with 37 CFR §§1.56, 1.97 and 1.98. The items listed on the accompanying Form PTO-1449 may be deemed to be pertinent to the above-identified application and are made of record to assist the Patent and Trademark Office in its examination of this application. The Examiner is respectfully requested to fully consider the items listed on the enclosed copy of Form PTO-1449 and to independently ascertain their teaching.

No fee is believed to be due under 37 CFR §1.17(p) for this Information Disclosure Statement since it is being filed concurrently with the above-identified application.

Respectfully submitted,

September 17, 2003
Date



Nathaniel Levin
Registration No. 34,860
Buckley, Maschoff, Talwalkar & Allison LLC
5 Elm Street
New Canaan, CT 06840
(203) 972-3460

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|---|---|----|---|--------------------------|------------------|
| Substitute for form 1449A/PTO | | | | Complete if Known | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | | | Application Number | Not yet assigned |
| | | | | Filing Date | Herewith |
| | | | | First Named Inventor | CHIU, Scott |
| | | | | Group Art Unit | Not yet assigned |
| | | | | Examiner Name | Not yet assigned |
| Sheet | 1 | of | 1 | Attorney Docket Number | P16585 |

| OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS | | | |
|---|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | A | "Optical Data Access Arrangement Circuit Application Note", January, 2000, CP Clare Corporation, LiteLink™ - CPC5604. 32pgs. | |
| | B | "3.3 V Global Direct Access Arrangement", Rev. 2.01, 9/01, Si3034-DS201, Copyright © 2001 by Silicon Laboratories. 66pgs. | |
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| Examiner Signature | | Date Considered | |
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.